"PCB DESIGN"

ADD ON COURSE REPORT

Conducted by

Department of Electrical & Electronics Engineering



SCMS School of Engineering and Technology

Karukutty - 683576

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Course Details

Name of the course	Add on course		
Conducted by	Department of Electrical & Electronics Engineering		
College	SCMS School of Engineering & Technology, Karukutty		
Eligibility	U.G Students		
Title of the course	PCB Design		
Duration of the course	5 days (30 hours)		
Class	S3 EEE		
Course dates	25 th Nov 2023, 26 th Nov 2023, 30 th Nov 2023,01 st Dec 2023, 07 th Dec 2023		
Sessions	Forenoon session: 8:45 am-11:45 am (3 hours)		
	Afternoon session: 12:30 pm - 3:30 pm (3 hours)		
	Total: 30 hours (6 hours per day)		
Course Co-ordinator	Ms. Megha Jasmin Benny, Assistant Professor		
Head of department	Dr. Jayanand B		

"Printed Circuit Board (PCB) Designing"

Objective of the Course

This is a basic course for designing of PCB using software. PCB (Printed Circuit Board) designing is an integral part of each electronics products and this program is designed to make students capable to design their own projects PCB up to industrial grade.

Topics Covered:

- 1. Introduction to PCB designing concepts
- 2. Component introduction and their categories
- 3. Introduction to Development Tools
- 4. Detailed description and practical of PCB designing
- 5. Lab practice and designing concepts

Objectives

- Identifying Electronic Components Symbols & Footprints
- Constructing your Component libraries & use them effectively
- Schematic creation & interpretation
- Effective use of design rules & interfacing between schematic & PCB
- Component placement & routing techniques for various technologies Outcomes
- Students are able to design a schematic of their circuit
- Students are able to design PCB layout of their design
- Students are capable to produce PCB of their own circuit
- The course is intended to give the students the necessary knowledge and of PCB design steps, starting from a simple schematic, through creating new components, and all the way to down a final PCB layout
- Recognize the technologies used in electronic industry through the practical experience gained in the course.

Software used

LTSpice

Purpose of the Course

The gap in syllabus for the subject "EET205 – Analog Electronics" was identified. An add on course titled "PCB Design" for a duration of 30 hours is planned to overcome the gap in syllabus.

Detailed syllabus

Sl	Unit Name	Content			
no					
1	Introduction to PCB designing	Introduction & brief history			
	concepts	• What is PCB?			
		• History			
		• Types of PCBs: Single Sided (Single layer),			
		Multi- Sided (Double layer)			
		PCB Materials The state of the state o			
		Introduction to Electronic Design Automation (EDA)			
		Brief History of EDA			
		Latest Trends in Market			
		How it helps & why it requires			
		Different EDA tools			
		Introduction to LTSpice Environment			
2	Component Introduction &	Types of components			
	their categories	1. Active Components			
		• Diode			
		• Transistor			
		• MOSFET			
		• LED			
		• SCR			
		 Integrated Circuits 			
		2. Passive Components			
		• Resistor			
		• Capacitor			
		• Inductor			
		Transformer			
3	PCB make	• What is PCB made of?			
		PCB Base			
		• FR-4			
4	PCB Characteristics	 Through hole technology 			
		 Through surface mount technology 			
5	Introduction to development	 Introduction to PCB design using LTSpice 			
	tools	software			
6	Detailed description & practical	PCB Fabrication • Set-up			
	of PCB designing				
		 Imaging 			
		• Etching			
		• Drilling			
		Masking			
		Silk screening			
		• Route			

		Electrical test
7	Practical work	Hardware/Software designing on PCB

Assessment pattern

Sl no	Name	Marks
1	Assignment	30
2	Examination	50
3	Viva	20

Attendance

SI no	Name of Student	Day 1 (8:45 am - 3:30 pm)	Day 2 (8:45 am - 3:30 pm)	Day 3 (8:45 am - 3:30 pm)	Day 4 (8:45 am - 3:30 pm)	Day 5 (8:45 am – 3:30 pm)	Final Marks (100)
1	ABHINAV RAJ	\boxtimes	\boxtimes	A	\boxtimes	\boxtimes	
2	ABHISHEK ACHUTHAN	\boxtimes	\boxtimes	\boxtimes	\boxtimes	\boxtimes	
3	AGIN K SIMON	\boxtimes	\boxtimes	\boxtimes	\boxtimes	\boxtimes	
4	AJMAL SHAJI	A	\boxtimes	\boxtimes	\boxtimes	\boxtimes	
5	AKSHAY BABU	\boxtimes	\boxtimes	\boxtimes	\boxtimes	\boxtimes	
6	ASHFIYA SALEEM C S	\boxtimes	\boxtimes	\boxtimes	\boxtimes	\boxtimes	
7	KEERTHANA P MENON	\boxtimes	\boxtimes	\boxtimes	\boxtimes	\boxtimes	
8	MUHSINA O M	\boxtimes	\boxtimes	\boxtimes	\boxtimes	\boxtimes	
9	NIJON ALEX	\boxtimes	\boxtimes	\boxtimes	A	\boxtimes	
10	NISSIN C P	\boxtimes	\boxtimes	\boxtimes	\boxtimes	\boxtimes	
11	PARVATHY RAMACHANDRAN	\boxtimes	\boxtimes	\boxtimes	\boxtimes	\boxtimes	
12	PRATUL RAMAKRISHNAN	\boxtimes	\boxtimes	\boxtimes	\boxtimes	\boxtimes	
13	REVANTH C S	\boxtimes	\boxtimes	\boxtimes	\boxtimes	\boxtimes	
14	SABARINATH S	\boxtimes	\boxtimes	\boxtimes	A	\boxtimes	
15	SANJO JAIJI	\boxtimes	A	\boxtimes	\boxtimes	\boxtimes	

Course Summary

PCB Design (30 Hours)

Five Days-30 hours Add On Course on "*PCB Design*" was organized by the EEE dept of SCMS School of Engineering & Technology during November/December 2023. This course aims to provide opportunities to students to enrich their technical knowledge in the field of PCB and its applications. The resource person was Ms. Megha Jasmin Benny, Assistant Professor, EEE dept. The Programme is also designed to make students capable to design their own projects PCB up to industrial grade. The course was opened to S3 EEE students. Fifteen students attended the program. The feedback of the sessions received by the participants was excellent.



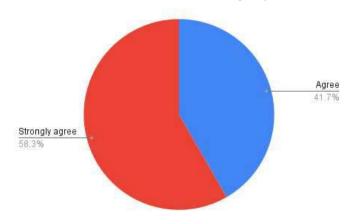
Fig (a): Talk on PCB design



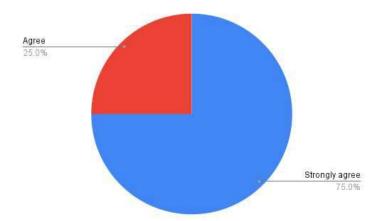
Fig (b): Hands on training on PCB design

Feedback

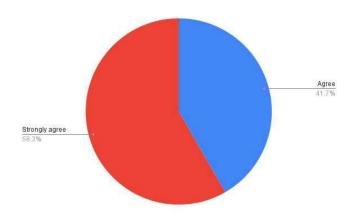
1. I achieved the training objectives



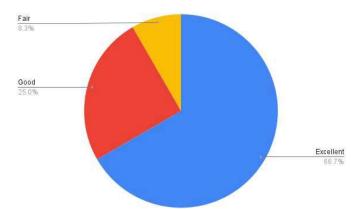
4. Gained practical knowledge



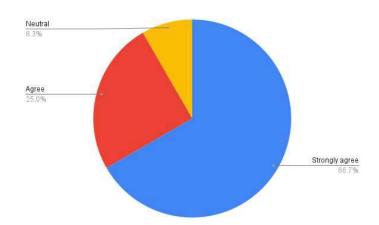
2. The instructions were clear & easy to follow



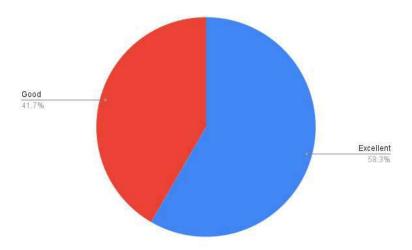
5. Trainer made the subject understandable



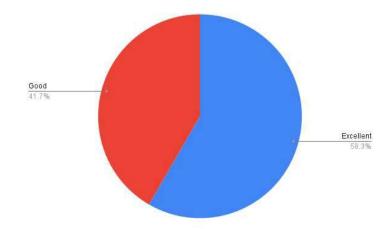
3. The presentation slides were clear



6. Trainer provided technical & practical knowledge



7. Overall training



Ms. Megha Jasmin Benny

Course Coordinator

Dr. Jayanand B

HOD