Vidya Nagar, Karukutty - 683 576 Time Table w.e.f. 24-01-2024

ELECTRONICS & COMMUNICATION ENGINEERING (S8) Batch 2020-24 Class Room: AC [20]

Day	8.45 - 9.45 am	9,45 - 10.45 am	11 - 12 am		12.45-1.40 pm	1.40 -2.35 pm	2.50 – 3.45 pm
Mon.	WC	RTOS	SC(T)	1	Proj	ect Phase II (PSK*.I	OKS,VVG)
Tue	RTOS	SC	MEC/REC	LUNCH	Project Phase II((AV,DKS,SAJ)		
Wed	Project Pl	hase II(PSK*,\	/VG,SAJ)	BREAK	WC	SC	MEC/REC(T)
Thu	Projec	t Phase II(AV.	Phase II(AV,LNM)		MEC/REC	Comprehensive Co	urse Work(AV,PSK
Fri	MEC/REC	SC	WC	100	RTOS	WC(T)	RTOS

Con	rse Code	Course Name	Name of Faculty		
M.C	ECT402	Wireless Communication	SAJ	Ms. Sreeja.K.A.,Dr Deepak KS(T)	
SC	ECT424	Satellite Communication	DKS	Dr Deepak KS,Ms. Neethu Susan Idiculay(T)	
RTOS	ECT426	Real Time Operating Systems	VJG	Mr. Vinoj P G,Ms Praveena S Kammath(T)	
REC	ECT 468	Renewable Energy Systems	PSK	Ms Praveena S Kammath, Mr. Vinoj P G(T)	
MEC	ECT 418	Mechatronics	AJ	Mr Ajit Joseph, Ms Nithya M(T)	
CCW	ECT404	Comprehensive Course Work	AV*,PSK	Ms Anandhi.V*,Praveena S Kammath	
PROJECT	ECD 416	Project Phase II	PSK*,AV,DKS	Ms Praveena S Kammath*, Ms Anandhi, V, Dr Deepak KS, Ms Vrinda V Gopal T, Ms Lekshmi Nair M	

Saturday's Time table will be on a rotation basis (Monday to Friday) and will be intimated well in advance.

Vision of the Department	To achieve academic excellence in Electronics and Communication Engineering and mould technically competent engineers by imparting quality education to the students while keeping in tune with the ever-changing industrial demands and societal needs.
	M1 Impart a solid foundation in the field of Electronics and Communication. M2 Provide education with emphasis on the ethics, leadership, and entrepreneurship necessary for students to become successful contributors to society. M3 To create individuals who can continue to learn on their own to scale greater heights in their profession.

HOD

Vidya Nagar, Karukutty - 683 576

Time Table w.e.f. 29-01-2024

ELECTRONICS & COMMUNICATION ENGINEERING (S6) Batch 2021-25 Class Room No:ACI 202

	LLL	of Romes a	COMMUNICATION	· DI OH LEIGH	10 (50) Baten	2021 20 0.000	110011111011101	
Day	8.45 - 9.35 am	9.35 - 10.25 am	10.35 - 11.30 am	11.30 -12.20 pm		1.05-1.55 pm	2.05 -2.55 pm	2.55 – 3.45 pm
Mon.	ITC	DIP	EM	VLSI		ITC	EM	MFE
Tue	M/R(ITC)	CL LAB	/ MP (SSJ*,CD/VJC	LUNCH	VLSI(T)	DIP	VLSI	
Wed	M/R(EM)	MFE	DIP	VLSI	BREAK	DIP	EM	ITC(T)
Thu	M/R(VLSI)	COMPREH	COMPREHENSIVE VIVA(NM*, VVG, NSI)			CL LAI	B / MP (SSJ*,CI	D/VJG*,LNM)
Fri	M/R(DIP)	ITC	EM	VLSI		MFE	ITC	EM(T)

C	ourse Code	Course Name	Name of Faculty			
EM	ECT 302	Electromagnetics	DB	Mr Dinesh Babu, Ms Sukanya Singh M J (T)		
VLSI	ECT 304	VLSI Circuit Design	PM	Dr Parvathy M,Ms Chaitanya Das (T)		
ITC	ECT 306	Information Theory And Coding	AV	Ms.Anandhi V,Ms.Vrinda V Gopal(T)		
DIP	ECT 300	Digital Image Processing	LNM	Ms Lekshmi Nair M		
MFE	HUT310	Management For Engineers		Dr. Anjana Viswanath		
CCW	ECT308	Comprehensive Course Work	NM*,VVG,NSI	Ms. Nithya.M * ,Ms.Vrinda V Gopal,Neethu Susan Idiculay		
		Communication Lab	SSJ*,CD	Ms Sukanya Singh*,Ms Chaitanya Das		
CL	ECL 332			Mr.Vinoj P G*, Ms Lekshmi Nair M		
MP	ECD 334	Mini Project		Ms.Vyshna RK		
WNIA	CST386	Wireless Networks & IOT Applications		timated well in advance.		

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Department	demands and societal needs. M1 Impart a solid foundation in the field of Electronics and Communication.
Mission of the	M2 Provide education with emphasis on the ethics, leadership, and entrepreneurship necessary for students to second
	successful contributors to society. M3 To create individuals who can continue to learn on their own to scale greater heights in their profession.

HOD

Vidya Nagar, Karukutty - 683 576

Time Table w.e.f. 29-01-2024

ELECTRONICS & COMMUNICATION ENGINEERING (S4) Batch 2022-26 Class Room AC I No: 203

Day	8.45 -9.35 am	9.35 -10.25 am	10.35 -11.30 am		12.15 -1.05 pm	1.05-1.55 pm	2.05 -2.55 pm	2.55 - 3.45 pm
Mon.	AC	SS	PRN		CAM	SS	PRN	CAM(T)
Tue	SS	PRN	AC	LUNCH	M/SS(R)	CAM	PE	AC(T)
Wed	PRN	CAM	CI	BREAK	M/PRN(R)	ACS /MO	C LAB (RG*,PM,	NSI/MC*,VJG)
Thu	CAM	PE	AC		M/CAM(R)	AC	SS	PRN
Fri	ACS	ACS /MC LAB (RG*,PM,NSI/MC*,VJG)			M/AC(R)	CI	Y2P-AT	SS(T)

Course Code		Course Name	Name of Faculty				
PRN	MAT204	Probability, Random Process & Numerical Methods		Dr. Nuja M. Unnikrishnan			
AC	ECT202	Analog Circuits	RG	Ms Retty George, Dr. Parvathy M(T)			
SS	ECT204	Signals & Systems		Mr Ajit Joseph, Ms Anandhi V(T)			
CAM	ECT206	Computer Architecture & Microcontrollers	327/42/42/	Ms.Mary Catherine,Ms Retty George(T)			
PE	HUT200	Professional Ethics	RGR	Dr. Raghav G R			
CI	MCN202	Constitution of India	RTR	Ms. Rony Tresa Rajesh Ms Retty George*, Dr.Parvathy M,Ms. Neethu Susan Idiculay			
ACS LAB	ECL202	Analog Circuits & Simulation Lab	RG*,PM	Ms.Mary Catherine*,Mr Vinoj PG			
MC LAB	ECL204	Microcontroller Lab	DIA	Ms.Deepa Johny			
ICN	CST286	Introduction to Computer Networks (Minor)	NMU	Dr. Nuja M. Unnikrishnan			
Y2P-AT	Y2P- AT	Youth to Power-Aptitude Training	CARROLL CO. 11 CO. 12				

Saturday's Time table will be on a rotation basis (Monday to Friday) and will be intimated well in advance.

Vision of the	To achieve academic excellence in Electronics and Communication Engineering and mould technically competengineers by imparting quality education to the students while keeping in tune with the ever-changing industrial
	engineers by imparting quanty education
Department	demands and societal needs.
Mission of the Department	M1 Impart a solid foundation in the field of Electronics and Communication. M2 Provide education with emphasis on the ethics, leadership, and entrepreneurship necessary for students to become successful contributors to society. M3 To create individuals who can continue to learn on their own to scale greater heights in their profession. HOD PRINCIPAL
	HOD

Vidya Nagar, Karukutty - 683 576

Time Table w.e.f. 29-01-2024

ELECTRONICS & COMMUNICATION ENGINEERING (S2) Batch 2023-27 Class Room No. AC1 107

	ELECTRON	ICS & COMIN	TUNICATION I	ENGINEERIN	G (S2) Batch	2023-27 Cla	ss Room No: A	AC1 107
Day	8.45 -9.35 am	9.35 -10.25 am	10.35 -11.30 am		12.15 -1.05 pm	1.05-1.55 pm	2.05 -2.55pm	2.55 - 3.45pm
Mon.	BCE	CP	VDT	1	EG	EG	PHY(TGP*, SN	MV)/CE(NMN*, RKR)
Tue	E	EG	PC	LUNCH	PHY	CP	CP	VDT
Wed	PHY	ME V	V/S (JSD)	BREAK	Y2P	BME	VDT	BCE
Thu	BME	PHY	VDT]	PC	BCE	VDT	EG
Fri	VDT	CP LAB(SS.	A*,DJY,X1)		BME	EG	PHY	CP

Course	Code	Course Name	Name of Faculty			
VDT	MAT 102	Vector Calculus, Differential equations & Transforms	ЈМЈ	Mrs. Jinu M.J.		
PHY	PHT 100	Engg. Physics A	TGP	Mr. Thomas George P.		
EG	EST 110	Engineering Graphics	VH	Mr. Vishnu H		
BCE	NAC SOLAN AGENCE	Basics of Civil Engineering	DR	Mr. Deepu R		
BME	EST120	Basics of Mechanical Engineering	AKR	Mr. Ajith Kumar R		
PC	HUN 102	Professional Communication	JT	Ms. Jane Theresa		
CP	EST 102	Programming in C		Ms. Shali Sara Abraham, Ms. Deepa Johny , X1		
PHY. LAB	PHL 120	Engg. Physics Lab		Mr. Thomas George P., Dr. Santhosh M. V.		
ME W/S	ESL 120	Mechanical Workshop	•	Mr. Jose Sheril D'Cotha		
Delication State of the Control of t	ESL 130	Civil & Mechanical W/S	NMN*, RKR			
EE & EC W/S Y2P	Y2P	Youth to Power	JT	Ms. Jane Theresa		

Saturday's Time table will be on a rotation basis (Monday to Friday) and will be intimated well in advance.

urday's Time table will be o	To achieve academic excellence in Electronics and Communication Engineering and mould technically competent engineers by imparting quality education to the students while keeping in tune with the ever-changing industrial demands and societal needs.
	M1 Impart a solid foundation in the field of Electronics and Communication. M2 Provide education with emphasis on the ethics, leadership, and entrepreneurship necessary for students to become successful
Mission of the Department	contributors to society. M3 To create individuals who can continue to learn on their own to scale greater heights in their profession.

Vidya Nagar, Karukutty - 683 576

Time Table w.e.f. 29-01-2024

ELECTRONICS ENGINEERING (VLSI DESIGN AND TECHNOLOGY) (S2) Batch 2022-26 CI

EL	ECTRONICS E	NGINEERING	(VLSI DESIGN A	ND TECH	NOLOGY) (S	2) Batch 2022-26	Class Room No:	AC1 110 B
Day	8.45 -9.35am	9.35 -10.25am	10.35 -11.30am		12.15 -1.05 pm	1.05-1.55 pm	2.05 -2.55pm	2.55 - 3.45pm
Mon.	BME	VDT	PC	1	CP Lab(AYM*,VRK)		EG	CP
Tue	PHY	CP	VDT	LUNCH	PHY	BCE	E	G
Wed	EG		VDT	BREAK	BCE	CP	ME W/S (DHS)	
Thu	BME	BCE	PHY	1	PC	VDT	BME	EG
Fri	VDT	PHY(SMV*, TGP)/CE (RYK)		1	CP	VDT	PHY	Y2P

Course Code		Course Name	Name of Faculty		
VDT	MAT 102	Vector Calculus, Differential equations & Transforms	NMU	Dr. Nuja M. Unnikrishnan	
PHY	PHT 100	Engg. Physics A	SMV	Dr. Santhosh M. V.	
EG	EST 110	Engineering Graphics	VC	Dr. Vidya Chandran	
BCE		Basics of Civil Engineering	AAM	Dr. Akhila M	
BME	EST 120	Basics of Mechanical Engineering	NA	Mr. Nikhil Asok	
PC	HUN 102	Professional Communication	DMS	Ms. Divya M. S.	
СР	EST 102	Programming in C	AYM*,VRK	Ms. Aiswarya Menon, MS. Vyshna R K	
		Engg. Physics Lab	SMV*, TGI	Dr. Santhosh M. V., Mr. Thomas George P.	
ME W/S	PHL 120 ESL 120	Mechanical Workshop	DHS	Mr. Dhanesh S	
		Civil & Mechanical W/S	RYK	Ms. Remya Y K	
E & EC W/S Y2P	ESL 130 Y2P	Youth to Power	DMS	Ms. Divya M. S.	

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Saturday's Time table will be	To achieve academic excellence in Electronics and Communication Engineering and mould technically competent engineers by imparting in achieve academic excellence in Electronics and Communication Engineering industrial demands and societal needs.
Vision of the Department	To achieve academic excellence in Electronics and Communication Engineering are a societal needs. quality education to the students while keeping in tune with the ever-changing industrial demands and societal needs.
	M1 Impart a solid foundation in the field of Electronics and Communication. M2 Provide education with emphasis on the ethics, leadership, and entrepreneurship necessary for students to become successful contributors