

RESOURCE PERSONS



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REGISTRATION



SCAN THE QR CODE
TO REGISTER



BANK ACCOUNT DETAILS

Account Name: SCMS School of Engineering & Technology

Account Number : 345801010030000

Bank : Union Bank of India

Branch : Pallissery

IFSC Code: UBIN0558885

Register @
Rs.200/-

For Queries :  **+91-9447701846**
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PATRONS

Dr. G. P. C. Nayar

Chairman

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Prof. Pramod P. Thevannoor

Executive Chairman

SCMS Group of Educational Institutions

INTERNAL ADVISORY COMMITTEE

Dr. Praveensal C. J., Group Director, SSET

Dr. Anitha G. Pillai, Principal, SSET

CONVENOR

Dr. Sreeja K. A., HoD, ECE

FDP CO ORDINATOR:

Dr. Anandhi V., Associate Professor, ECE

Mr. Unnikrishnan P., Assistant Professor, ECE

FACULTY DEVELOPMENT PROGRAMME

ON

AI Driven VLSI, Electronics Automation, Communication and Intelligent System Design

Organized by
Department of Electronics and
Communication Engineering



16-06-2026 to 20-06-2026



10.00 AM - 12.00 PM IST



Mode : Online

in association with IQAC



SCMS SCHOOL OF ENGINEERING
AND TECHNOLOGY (AUTONOMOUS)
Vidya Nagar, Pallissery, Karukutty, Ernakulam - 683576, Kerala, India.
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ABOUT THE COLLEGE

SCMS School of Engineering and Technology (SSET) is envisaged as a premier institution offering technology related education of exceptional quality to students by developing their total personality with due emphasis on ethical values and preparing them to meet the growing challenges of the industry and diverse societal needs of the institution. SSET, promoted by the SCMS Group of Educational Institutions, has been in the forefront of providing quality professional education in Engineering & Technology since 2001. SSET has been established at Karukutty in Ernakulam District, equipped with state-of-the-art facilities that foster academic excellence, innovation, and holistic development. SSET is one among the first ten colleges to be set up in the state under the private self-financing sector. Right from the beginning SSET concentrated on providing quality education in a highly disciplined environment. This has paid rich dividends over the past years, as evidenced by the preference of students to join the college.

ABOUT THE DEPARTMENT

The Department of Electronics and Communication Engineering was established in the year 2001. The department offers two B.Tech Programmes: B.Tech in Electronics & Communication Engineering, B.Tech in Electronics Engineering (VLSI Design and Technology). The department has also been recognized as a research centre for Ph.D Programmes affiliated to APJ Abdul Kalam Technological University (KTU). Well experienced faculty members with expertise in the core areas of VLSI design, Wireless Communication, Signal Processing, Electronic Circuits, and Embedded Systems work as a team to nurture students in a conducive academic and research environment.

VISION

To become a premier Electronics Engineering Department driving innovation, research, and technological leadership for sustainable societal advancement.

MISSION

- Impart quality Electronics Engineering education by strengthening core fundamentals and engaging with industry and society.
- Conduct and promote research in Electronics Engineering to develop practical and innovative technological solutions.
- Develop socially responsible professionals with ethical leadership qualities.

ABOUT THE FDP

This FDP explores the transformative role of Artificial Intelligence in VLSI design, electronics automation, communication systems, and intelligent technologies. Participants will gain insights into emerging trends, advanced design methodologies, smart system development, and innovative research opportunities, fostering expertise in next-generation engineering solutions. This provides a platform for knowledge sharing between academia and industry experts.

OBJECTIVES OF FDP

- To provide insights into the application of Artificial Intelligence in VLSI design, electronics automation, communication systems, and intelligent system development.
- To familiarize participants with emerging tools, technologies, and industry trends that enhance efficiency, innovation, and automation in modern electronic and communication engineering.
- To strengthen the research and technical capabilities of faculty members by promoting interdisciplinary approaches to intelligent system design and AI-enabled engineering solutions.

ELIGIBILITY

- The FDP is open to faculty members from AICTE-approved institutions only.
- Registration Fee: ₹200 per participant.
- Registration will be confirmed on a first-come, first-served basis.
- Last date for registration: 15-06-2026