

SCMS SCHOOL OF ENGINEERING AND TECHNOLOGY

Vidya Nagar, Karukutty - 683 576

Time Table w.e.f. 01-07-2025 (Revised on 18-07-2025)

Time Table W.E.T. 01-07-2025 (Revised on 18-07-2025)

ELECTRONICS & COMMUNICATION ENGINEERING (S7) - Class Room No. - AC1 201							
Day	8.45 - 9.45 am	9.45 - 10.45 am	11.00am - 12.00pm	LUNCH BREAK	12.45-1.40 pm	1.40 -2.35 pm	2.50 - 3.45 pm
Mon.	OFC/CN	OE	OFC/CN		EMS LAB (NM*, SSM) / SEM (AV*,PM)		
Tue	HON/ISE(R)	MAC	OE		PROJECT PHASE I (RG,CJ,DKS)		
Wed	EMS LAB (NM*, SSM)/ SEM (AV*,PM)				OE	MAC	OFC/CN(T)
Thu	OFC/CN(R)	OFC/CN	OE		ISE	MAC(T)	ISE
Fri	HON/MAC(R)	ISE	MAC		PROJECT PHASE I (RG*,SSM)		

Course Code		Course Name	Name of Faculty	
MAC	ECT401	Microwaves and Antennas	SSM	Ms. Sukanya Singh M J
OFC	ECT413	Optical Fibre Communication	CJ	Ms. Chelsy Joy
CN	ECT 423	Computer Networks	HRP	Ms.Hridya R P
OE	CET 445	Natural Disasters and Mitigation	PAV	Dr. Praseeja A V
ISE	MCN401	Industrial Safety Engineering	RG	Ms. Retty George
EMS Lab	ECL411	Electromagnetics Lab	NM*, SSM	Ms. Nithya M*, Ms. Sukanya Singh M J
SEM	ECQ413	Seminar	AV*,PM,VJG	Dr. Anandhi.V*,Dr. Parvathy M
PP-I	ECD415	Project Phase I	RG*,DKS,CJ, SSM	Ms. Retty George*,Dr.Deepak K S, Ms.Sukanya Singh M J, Ms.Chelsy Joy
MIN	CSD481	Mini Project (B2)	SKK	Ms. Sareena K K
		Mini Project (B1)	SKJ	Ms. Sruthy K Joseph
HON	ECT 495	RF MEMS	SAJ	Dr. Sreeja K A
REM		Remedial	-	-

Saturday's Time table will be on a rotation basis (Monday to Friday) and will be intimated well in advance.


HoD


Principal

Vision of the Department	To achieve academic excellence in Electronics and Communication Engineering and mould technically competent engineers by imparting quality education to the students while keeping in tune with the ever-changing industrial demands and societal needs.
Mission of the Department	M1 Impart a solid foundation in the field of Electronics and Communication.
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Time Table w.e.f. 01-07-2025 (Revised on 19-07-2025)

ELECTRONICS & COMMUNICATION ENGINEERING (S5) - Class Room - AC1 202								
Day	8.45 - 9.35 am	9.35 - 10.25	10.35 - 11.30	11.30 - 12.20	LUNCH BREAK	1.05-1.55	2.05 -2.55	2.55 - 3.45
Mon.	DSP	DSP LAB(SV*,SAJ)/AIC LAB(LNM*,NM)				CS	DM	LIC(T)
Tue	MIN/ ADC(R)	DM	ADC	DSP		LIC	DSP	CS(T)
Wed	MIN/ CS(R)	IEFT	CS	DM		ADC	DSP	ADC(T)
Thu	MIN/ DSP(R)	CS	LIC	IEFT		DSP LAB(SV*,SAJ)/AIC LAB (LNM*,NM)		
Fri	MIN/ LIC(R)	ADC	CS	LIC		ADC	IEFT	DSP(T)

Course Code		Course Name	Name of Faculty	
LIC	ECT 301	Linear Integrated Circuits	DKS	Dr. Deepak K S
DSP	ECT 303	Digital Signal Processing	AV	Dr. Anandhi.V
ADC	ECT 305	Analog and Digital Communication	NM	Ms. Nithya M
CS	ECT 307	Control Systems	AJ	Mr. Ajit Joseph
IEFT	HUT300	Industrial Economics & Foreign Trade	DMS	Ms. Divya M S
DM	MCN301	Disaster Management	LNM	Ms. Lekshmi Nair M
AIC LAB	ECL 331	Analog Integrated Circuits and Simulation Lab	LNM* NM	Ms. Lekshmi Nair M*, Ms. Nithya M
DSP LAB	ECL 333	Digital Signal Processing Lab	SV*,SAJ	Ms. Sandhya Venugopal*, Dr.Sreeja K A
R/M	CST-383	Concepts in Machine learning	X	X
REM	REM	Remedial		

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Time Table w.e.f. 01-07-2025 (Revised on 14-07-2025)

Time Table w.e.f. 01-07-2025 (Revised on 14-07-2025)

ELECTRONICS ENGINEERING (VLSI Design and Technology) (S5) - Class Room - AC1 207								
Day	8.45 - 9.35	9.35 - 10.25	10.35 - 11.30 am	11.30 - 12.20	LUNCH BREAK	1.05-1.55	2.05 -2.55	2.55 - 3.45
Mon.	LIC	RC LAB (MC*,PSK)				FSD	IEFT	DCD(T)
Tue	LIC(R)	DM	LIC	DCD		VT	DCD	FSD(T)
Wed	FSD(R)	IEFT	FSD	VT		AIC LAB (PM*,RG)		
Thu	VT(R)	DCD	LIC	DCD(R)		DM	VT	LIC(T)
Fri	VT	FSD	LIC	FSD		DM	IEFT	VT(T)

Course Code			Name of Faculty	
LIC	ECT 301	Linear Integrated Circuits	PM	Dr. Parvathy M
FSD	EVT 303	FPGA based System Design	SAJ	Dr. Sreeja K A
DCD	EVT 305	Digital CMOS Design	VVG	Ms Vrinda V Gopal T
VT	EVT 307	VLSI Technology	AMJ	Ms.Amruha Joshy
IEFT	HUT 300	Industrial Economics & Foreign Trade	RTD	Ms. Rony Tresa Davis
DM	MCN301	Disaster Management	AV	Dr. Anandhi.V
AIC LAB	ECL 331	Analog Integrated Circuits and Simulation Lab	PM*,RG	Dr. Parvathy M*,Ms Retty George
RC LAB	EVT 333	Reconfigurable Computing Lab	MC*,PSK	Ms Mary Catherine V G*,Ms Praveena S Kammath
REM	REM	Remedial		

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Time Table w.e.f. 01-07-2025 (Revised on 13-08-2025)

ELECTRONICS & COMMUNICATION ENGINEERING (S3) - Class Room No. - AC1 205

	1	2	3		4	5	6	7
Day	8.45 -9.35	9.35 -10.25	10.35 -11.30		12.15 -1.05pm	1.05-1.55	2.05 -2.55	2.55 - 3.45
Mon.	IAIDS	LCD	MEPS-3	LUNCH BREAK	SSD	EESD	Y2P	LCD(T)
Tue	LCD LAB(AJ*,VVG)/AC LAB(CJ*,AMJ)				MIN/REM (LCD)	IAIDS	MEPS-3	SSD(T)
Wed	MEPS-3	AC	EESD		MIN/REM (AC)	AC	LCD	IAIDS
Thu	AC	SSD	AC(T)		MIN/REM(MEPS-3)	SSD	EESD	IAIDS
Fri	LCD	MEPS-3	IAIDS		MIN/REM (SSD)	LCD LAB(AJ*,VVG)/AC LAB(CJ*,UKP)		

Course Code		Course Name	Name of Faculty	
MEPS-3	GYMAT301	Mathematics for Electrical Science and Physical Science	MVK	Mr. Mohamed Akbar
SSD	PCECT302	Solid State Devices	PM	Dr.Parvathy.M
AC	PCECT303	Analog Circuits	DM	Mr Dinesh Babu
LCD	PBECT304	Logic Circuit Design	AJ	Mr Ajit Joseph
IAIDS	GNEST305	Introduction to Artificial Intelligence and Data Science	SVJ	Ms. Sandra Vijumon
EESD	UCHUT347	Engineering Ethics and Sustainable Development	RKR	Ms. Roshni K R
AC LAB	PCECL307	Analog Circuits Lab	CJ*,AMJ,SSM	Ms.Chelsy Joy*, Ms. Amurtha Joshy, Mr. UnniKrishnan P
LCD LAB	PCECL308	Logic Circuit Design Laboratory	AJ*,VVG	Mr Ajit Joseph*, Ms Vrinda V Gopal T
Y2P		Youth to Power	JT	Ms Jane Theresa
MINOR	MNMET309	Introduction to product design and Associated Development Tools	DX	Mr. Deepak Xavier
	MNCST309	Python for Application Development	AJE	Mr. Anoop Jose
	MNCST319	Python for Artificial Intelligence	AB	Ms. Anita Babu
	MNEET309	Introduction to Power System	BP,DNK,SG,AT	Dr. Beena Puthilath, Dr Divya Nath K, Mrs. Shereena Gafoor, Dr Anna Thomas

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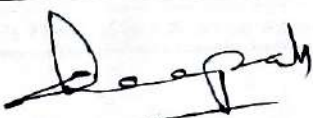
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Time Table w.e.f. 01-07-2025 (Revised on 13-08-2025)

ELECTRONICS ENGINEERING (VLSI Design and Technology)-S3 - Class Room No. - AC1 206								
	1	2	3	LUNCH BREAK	4	5	6	7
Day	8.45 -9.35	9.35 -10.25	10.35 -11.30		12.15 -1.05pm	1.05-1.55	2.05 -2.55	2.55 - 3.45
Mon.	AC	MEPS-3	SSD		EESD	IAIDS	AC(T)	IAIDS
Tue	IAIDS	LCD	AC		MIN/MEPS-3(R)	LCD LAB(HRP*,NM) /AC LAB(NSI*,MC)		
Wed	MEPS-3	SSD	IAIDS		MIN/AC(R)	SSD	Y2P	EESD
Thu	LCD	MEPS-3	LCD		MIN/SSD(R)	IAIDS	AC	SSD(T)
Fri	LCD LAB (HRP*,AV) /AC LAB(NSI*,MC)				MIN/LCD(R)	EESD	MEPS-3	LCD(T)

Course Code		Course Name	Name of Faculty	
MEPS-3	GYMAT301	Mathematics for Electrical Science and Physical Science	NMU	Dr. Nuja M Unnikrishnan
SSD	PCECT302	Solid State Devices	NSI	Ms.Neethu Susan Idiculay
AC	PCECT303	Analog Circuits	MC	Ms Mary Catherine V G
LCD	PBECT304	Logic Circuit Design	PSK	Ms.Praveena S Kammath
IAIDS	GNEST305	Introduction to Artificial Intelligence and Data Science	SVJ	Ms. Sandra Vijumon
EESD	UCHUT347	Engineering Ethics and Sustainable Development	SMR	Ms. Sruthy M R
AC LAB	PCECL307	Analog Circuits Lab	NSI*,MC	Ms. Neethu Susan Idiculay*,Ms Mary Catherine V G
LCD LAB	PCECL308	Logic Circuit Design Laboratory	HRP*,AV,NM	Ms.Hridya R P* ,Dr. Anandhi. V, Ms Nithya M
Y2P		Youth to Power	JT	Ms Jane Theresa
Minor	MNCST309	Python for Application Development	AJE	Mr. Anoop Jose
	MNCST319	Python for Artificial Intelligence	AB	Ms. Anita Babu

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Time Table w.e.f.13-08-2025

Time Table w.e.f.13-08-2025

ELECTRONICS & COMMUNICATION ENGINEERING (Group-B, S1) - Class Room :AC1 107								
	1	2	3	LUNCH BREAK	4	5	6	7
Day	8.45 -9.35	9.35 -10.25	10.35 -11.30		12.15 -1.05pm	1.05-1.55	2.05 -2.55	2.55 - 3.45
Mon.	EGR		ATP		PES	MEPS	MEPS	IEC
Tue	MEPS	ATP Lab(AED*,NR,SVJ)			IEE	HW	PES	IEE
Wed	IEC	IEE	MEPS		EE(RAV*) /EC(CJ*,SAJ)/ PHY(GR*,KKV)			
Thu	PES	ATP	IEE		EGR	EGR	EEE(RAV*)/EC(CJ*,UKP)	
Fri	MEPS	EGR	PES		IEC	ATP	HW	PES

Course Code		Course Name		Name of Faculty
MEPS	GYMAT101	Mathematics for Electrical Science and Physical Science-I	PTS	Ms.Preema T S
PES	GBPHT121	Physics for Electrical Science	KKV*,GR	Dr.Kannan Nithin K V,Dr. Geethu R
EGR	GMEST103	Engineering Graphics and Computer Aided Drawing.	NA	Mr. Nikhil Asok
IEE	GXEST104	Introduction to Electrical Engineering	RAV	Ms Rajeswary A V
IEC		Introduction to Electronics Engineering	CJ	Ms.Chelsy Joy
ATP	UCEST105	Algorithmic Thinking with Python	AED*,NR,SVJ	Ms. Anna Eldho, Ms. Neethu Roy,Ms. Sandra Vijumon
EE W/S	GXESL106	Basic Electrical Engineering Workshop	RAV	Ms Rajeswary A V
EC W/S		Basic Electronics Engineering Workshop	CJ*,UKP,SAJ	Ms Chelsy Joy*, Mr. UnniKrishnan P , Ms. Sreeja K A
HW	UCPWT127	Health and Wellness	NS	Ms.Niya Suku
	Y2P	Youth to Power		
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Time Table w.e.f.13-08-2025

Time Table w.e.1.15-08-2025

ELECTRONICS ENGINEERING (VLSI Design and Technology) - (Group-B, S1) - Class Room :AC1 106								
	1	2	3	LUNCH BREAK	4	5	6	7
Day	8.45 -9.35	9.35 -10.25	10.35 -11.30		12.15 -1.05pm	1.05-1.55	2.05 -2.55	2.55 – 3.45
Mon.	PES	EE(SG*)/EC(NSI*,UKP)/ PHY(GR*,KKV)			ATP	MEPS	EGR	
Tue	ATP	IEE	IEC		PES	IEE	MEPS	HW
Wed	MEPS	PES	IEE		ATP	EGR		IEC
Thu	IEE	MEPS	EGR		ATP Lab(AED*,SSG,BJ)		HW	PES
Fri	EGR		MEPS		IEC	PES	EE(SG*)/EC(NSI*,HRP)	

<u>Course Code</u>		<u>Course Name</u>		<u>Name of Faculty</u>
MEPS	GYMAT101	Mathematics for Electrical Science and Physical Science-I	MT	Dr.Mini Tom
PES	GBPHT121	Physics for Electrical Science	GR*, KKV	Dr.Geethu R,Dr.Kannan Nithin K V
EGR	GMEST103	Engineering Graphics and Computer Aided Drawing.	SUR	Mr. Suraj R
IEE	GXEST104	Introduction to Electrical Engineering	RAV	Ms Rajeswary A V
IEC		Introduction to Electronics Engineering	SV	Ms.Sandhya Venugopal
ATP	UCEST105	Algorithmic Thinking with Python	AED*,SSG,BJ	Ms. Anna Eldho, Ms. Surya S G,Ms. Binu John
EE W/S	GXESL106	Basic Electrical Engineering Workshop	SG*	Ms Shereena Gafoor
EC W/S		Basic Electronics Engineering Workshop	NSI*,NM,RCV	Ms. Neethu Susan Idiculay*, , Mr. UnniKrishnan P, Ms.Hridya R P
HW	UCPWT127	Health and Wellness	SK	Mr.Sunikumar A
	Y2P	Youth to Power		
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