



SCMS SCHOOL OF ENGINEERING AND TECHNOLOGY (AUTONOMOUS)

Vidya Nagar, Palissery, Karukutty, Ernakulam - 683576, Kerala, India.
www.scmsgroup.org/sset

Minutes of the Meeting

Name of the meeting: Board of Studies		Meeting type: Offline/Hybrid/Online
Date: 15/7/2026		Time: 1:00 PM
Department: Electronics and Communication Engineering		
Meeting Link: meet.google.com/ypa-isws-zcc		
Attendees		
Sl.No	Category	Name
1	HoD (Chairperson)	Dr.Sreeja K A
2	Subject Experts	Dr. Harigovindan V.P. Dean Research and Consultancy, Professor, NIT Puduchery
		Dr. Ajayan K. R. Professor and Dean(Student affairs) (Rtd) CET Thiruvananthapuram
3	Expert Nominated by Vice Chancellor	Dr. B. Bhuvan Assistant Professor (Grade I), Department of Electronics and Communication Engineering, NIT Calicut
4	Representative from Industry	Mr. Prathaban Selvaraj Engineering Leader , Serverless, Google
5	Alumni Representative	Mr. Unnikrishnan Ambady Assistant Manager, Honeywell, Bangalore

Faculty Members from the Department

Sl. No	Name	Designation
1	Dr. Anandhi.V.	Associate Professor
2	Dr. Parvathy.M.	Associate Professor
3	Ms.Vrinda V.Gopal T	Assistant Professor
4	Ms. Praveena S. Kammath	Assistant Professor
5	Ms. Nithya M.	Assistant Professor
6	Ms. Retty George	Assistant Professor
7	Ms. Lekshmi Nair M.	Assistant Professor
8	Ms. Mary Catherine V. G.	Assistant Professor
9	Ms. Sukanya Singh M. J.	Assistant Professor
10	Mr. Ajit Joseph	Assistant Professor
11	Mr. Dinesh Babu M.	Assistant Professor
12	Ms. Neethu Susan Idiculay	Assistant Professor
13	Ms. Sandhya Venugopal	Assistant Professor
14	Ms. Chelsy Joy	Assistant Professor
15	Ms. Amrutha Joshy	Assistant Professor
16	Ms Athira Anilan	Assistant Professor
17	Ms Shaila C K	Assistant Professor
18	Ms Vishnupriya V	Assistant Professor
19	Ms Hridya R P	Assistant Professor
20	Mr Unnikrishnan P	Assistant Professor
21	Ms. Lakshmi Raj V	Assistant Professor

Sl. No	Agenda
1	Welcome Address by the Chairman
2	Introduction of Members of the Board of Studies and Confirmation of Quorum
3	Constitution of the Board of Studies and Briefing on the Roles and Responsibilities of the Board
4	Presentation of the Vision, Mission, Programme Educational Objectives(PEOs), Program Outcomes (POs), Programme Specific Outcomes(PSOs) and achievements of the Department
5	Approval of Curriculum for • B.Tech in Electronics and Communication Engineering • B.Tech in Electronics Engineering (VLSI Design and Technology)
6	Approval of syllabus formulated by the Department of Electronics and communication Engineering namely: • S1- 26GXEST104 - Introduction to Electrical & Electronics Engineering (Part-B: Module 3 and 4) • S1- 26GXESL106 - Basic Electrical and Electronics Engineering Workshop (Part-B) • S1- 26GXESL206 - IOT Workshop • S2- 26PCECT204 - Network theory • S2- 26GBEST203 - Foundations of Artificial Intelligence and Data Science
Sl. No	Discussions
1	Dr. Harigovindan V. P. suggested removing the Introduction to Electrical & Electronics Engineering course from the ECE programme, as the required fundamentals can be covered through discipline-specific courses. He observed that the course would be useful only if branch change is permitted. Department's Response: The existing curriculum has been retained. The Introduction to Electrical & Electronics Engineering course is offered as an Engineering Science Course (ESC) in accordance with the common first-year curriculum framework adopted across all

	B.Tech programmes. It is a common course for Electronics & Communication Engineering, Electrical & Electronics Engineering, and Computer Science & Engineering students, providing a uniform foundation in electrical circuits, electronic devices, and basic electrical and electronic systems before students progress to discipline-specific courses.
2	Dr. B. Bhuvan suggested that Analog Circuits should not be offered in the same semester as Solid State Devices (SSD) and Network Theory (NT), as these courses are prerequisites. Dr. Harigovindan V. P. recommended offering Analog Circuits after SSD and NT, preferably in Semester 4. Department's Response: The suggestion is accepted. To provide the required prerequisite knowledge before Analog Circuits, Network Theory has been shifted from Semester 3 to Semester 2, while Logic Circuit Design has been moved from Semester 2 to Semester 3. This revised sequence ensures that students complete Network Theory before studying Analog Circuits, thereby improving the academic progression of the curriculum.
3	Dr. Harigovindan V. P. suggested renaming the Mathematics courses to better reflect their contents. Department's Response: The suggestion is accepted. The Mathematics courses will be renamed as follows: Semester 1: Matrices, Ordinary Differential Equations and Transforms Semester 2: Partial Differential Equations and Vector Calculus Semester 3: Complex Analysis and Integral Transforms Semester 4: Probability Distributions, Statistical Inference and Random Processes
4	Dr. B. Bhuvan suggested renaming Fundamentals of Microcontroller to reflect the inclusion of Computer Architecture and Organization topics. Department's Response: The suggestion is accepted. The course has been renamed Fundamentals of Computer Architecture and Microcontroller to better reflect the revised syllabus and its expanded scope.

5	Dr. Harigovindan V. P. suggested offering Microprocessors and Microcontrollers before Computer Architecture and Organization and recommended referring to the Anna University curriculum during syllabus revision. Department's Response: The suggestion has been partially accepted. The course has been renamed Fundamentals of Computer Architecture to better reflect its revised scope. During the detailed syllabus revision, the sequencing of Computer Architecture will be reviewed with reference to the Anna University curriculum.
6	Mr. Prathaban Selvaraj suggested placing greater emphasis on computer and robotics programming and appreciated the overall structure of the proposed curriculum. Department's Response: The suggestion is appreciated. Programming skills are progressively developed through the IoT Workshop in Semester 2, which introduces AI-assisted programming for ESP32-based IoT applications, followed by Python Programming in the Scientific Computing Lab in Semester 3. In addition, a robotics-focused elective stream has been incorporated for students interested in robotics and intelligent systems.
7	Dr. B. Bhuvan suggested replacing Estimation and Detection in the Semester 4 Program Electives with a more suitable course, since Signals and Systems is offered in the same semester. Dr. Harigovindan V. P. suggested offering Speech and Audio Processing only after Digital Signal Processing (DSP) and recommended reviewing the prerequisite structure of all Program Electives, Minor and Honours courses. Department's Response: The suggestions are accepted. During curriculum revision, Program Electives were advanced from Semester 5 to Semester 4 to align with the institution's common curriculum framework. Consequently, some electives were shifted without adequate review of prerequisite requirements. The Semester 4 elective basket will be redesigned with appropriate introductory electives, while advanced electives will be repositioned to ensure proper prerequisite sequencing and academic progression across all elective streams.

8	Dr. Harigovindan V. P. suggested replacing Advanced Communication Theory with Computer Networks and Mobile Communication as core courses. Department's Response: The suggestion was discussed in detail. The Board resolved to retain Advanced Communication Theory as a core course since it provides the theoretical foundation for modern communication systems through topics such as Information Theory, Channel Coding, OFDM, MIMO, Wireless Communication, and Fading Channels. Computer Networks and Advanced Mobile Communication will continue as Program Electives, enabling students to specialize according to their interests while maintaining flexibility across multiple elective streams.
9	Dr. Harigovindan V. P. suggested shifting one course from Semester 8 to Semester 7 to facilitate internships. Department's Response: The existing curriculum structure has been retained. The reduced credit load in Semesters 7 and 8 is intentional to facilitate internships. Students may undertake a six-month internship in either Semester 7 or Semester 8 while completing the Major Project in the remaining semester, thereby providing flexibility and enhancing industry exposure.
10	Dr. Harigovindan V. P. suggested introducing multiple Honours programmes aligned with industry requirements. Dr. B. Bhuvan suggested offering Honours specializations based on faculty expertise. Department's Response: The suggestions are accepted. Multiple Honours specialization streams will be introduced based on emerging technologies, industry requirements, and faculty expertise. Care will be taken to avoid overlap between Honours courses and Program Electives.
11	Dr. Ajayan K. R. suggested combining Analog Circuits and Linear Integrated Circuits into a single course and introducing an RTL-to-GDSII course in a higher semester.

	Department's Response: The suggestion is accepted. Analog Circuits and Linear Integrated Circuits have been merged into a single course titled Analog Integrated Circuits in Semester 3. In addition, a new course titled Design Flow from RTL to GDSII has been introduced in Semester 6.
12	Dr. Ajayan K. R. suggested including RISC-V Architecture and FPGA Design in the curriculum. Department's Response: The suggestion is accepted. RISC-V Architecture will be incorporated into the FPGA-Based System Design course offered in Semester 6.
13	Dr. Ajayan K. R. suggested introducing a Skill Development Course on AI Tools or Prompt Engineering and replacing the Introduction to Electrical & Electronics Engineering course with Python Programming, observing that Programming in C alone is insufficient. Department's Response: The existing curriculum already incorporates AI-assisted programming through the IoT Workshop in Semester 2 and Python Programming through the Scientific Computing Lab in Semester 3. In addition, Skill Enhancement MOOC courses offered from Semesters 3 to 6 provide students with opportunities to pursue specialized courses in AI Tools, Prompt Engineering, and other emerging technologies.
14	Dr. Harigovindan V. P. raised a concern regarding offering the IoT Workshop in Semester 2, questioning whether students would have adequate background in sensors. Department's Response: The concern was noted. The foundational concepts of sensors and electronic devices required for the IoT Workshop are introduced through the Introduction to Electrical & Electronics Engineering course offered in Semester 1.
15	Dr. Ajayan K. R. suggested replacing Digital System Design (DSD) with a more advanced VLSI-related course.

	Department's Response: The suggestion was discussed. The Digital System Design course has been retained, as it provides essential foundations in digital circuit design, hazards, fault analysis, hardware description languages, and digital system implementation, which are prerequisite knowledge for advanced VLSI courses.
16	Dr. B. Bhuvan suggested replacing X-Circuit software with a PCB Design EDA tool. Department's Response: The suggestion has been partially accepted. While X-Circuit has been retained for schematic drawing and technical documentation, the PCB design EDA tool KiCad has been incorporated into the syllabus to strengthen students' practical design skills.
Sl. No	Resolutions
1	Resolved to retain the Introduction to Electrical & Electronics Engineering course as a common Engineering Science Course (ESC) in the first-year curriculum to provide a common foundation for students across allied engineering programmes.
2	Resolved to approve the revision of the course sequence by offering Network Theory in Semester 2 and Logic Circuit Design in Semester 3. This change ensures that the prerequisite knowledge required for Analog Circuits is acquired before the course is undertaken.
3	Resolved to rename the Mathematics courses to better reflect their respective course contents, as follows: Semester 1: Matrices, Ordinary Differential Equations and Transforms Semester 2: Partial Differential Equations and Vector Calculus Semester 3: Complex Analysis and Integral Transforms Semester 4: Probability Distributions, Statistical Inference and Random Processes
4	Resolved to rename the course "Fundamentals of Microcontroller" as "Fundamentals of Computer Architecture" to accurately reflect the revised syllabus.
5	Resolved to review the sequencing of Computer Architecture and Microprocessors & Microcontrollers during the detailed syllabus revision, with reference to the Anna University curriculum.

6	Resolved to strengthen programming education through the existing curriculum structure comprising the IoT Workshop, Scientific Computing Lab, and robotics-oriented elective stream.
7	Resolved to redesign the Semester 4 Program Elective basket by ensuring appropriate prerequisite sequencing, introducing suitable introductory electives, and repositioning advanced electives, including Speech and Audio Processing, to appropriate semesters.
8	Resolved to retain Advanced Communication Theory as a Program Core course, while continuing to offer Computer Networks and Advanced Mobile Communication as Program Electives.
9	Resolved to retain the existing Semester 7 and Semester 8 credit distribution to facilitate internships and provide flexibility for internship and major project scheduling.
10	Resolved to strengthen the Honours programme by introducing multiple specialization streams aligned with emerging technologies, industry requirements, and faculty expertise, while avoiding overlap with Program Electives.
11	Resolved to merge Analog Circuits and Linear Integrated Circuits into a single course titled Analog Integrated Circuits in Semester 3 and to introduce a new course titled Design Flow from RTL to GDSII in Semester 6.
12	Resolved to incorporate RISC-V Architecture into the FPGA-Based System Design course offered in Semester 6.
13	Resolved to continue AI-assisted programming and Python programming through the existing curriculum and to utilize the Skill Enhancement MOOC courses for offering AI Tools, Prompt Engineering, and other emerging technology courses.
14	Resolved to retain the IoT Workshop in Semester 2, as the required foundational knowledge of sensors and electronic devices is provided through the Semester 1 Engineering Science Course.
15	Resolved to retain Digital System Design as a core course, recognizing its importance in providing the foundational knowledge required for advanced VLSI studies.
16	Resolved to incorporate the PCB Design EDA tool KiCad into the curriculum, while retaining X-Circuit for schematic drawing and technical documentation.
17	Resolved that all curriculum revisions and syllabus modifications shall be incorporated in the final curriculum document after ensuring compliance with the institutional curriculum framework and academic requirements.

Sl. No	Recommendations to Academic Council
1	• Recommend approval of the revised B.Tech in Electronics and Communication Engineering curriculum structure, as approved by BoS. • Recommend approval of the revised B.Tech in Electronics Engineering (VLSI Design and Technology) curriculum structure, as approved by BoS.
2	Recommend approval of the revised course titles for Mathematics courses and following new courses for B.Tech in Electronics and Communication Engineering and B.Tech in Electronics (VLSI Design and Technology): • 26GXESL206 - IOT Workshop (For S2) • 26GREST305 - Fundamentals of Computer Architecture (For S3) • 26PCECL308 - Scientific Computing Lab (for S3)
3	Recommend approval of the revised Honours Programme with multiple specialization streams aligned with emerging technologies and industry requirements.
4	Recommend approval of the revised curriculum and authorize incorporation of all approved syllabus revisions into the final curriculum document in accordance with the institutional curriculum framework and academic regulations.

Head of the Department, ECE, SSET


19/8/2026

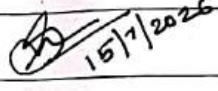
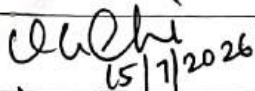
HEAD OF DEPARTMENT
DEPT. OF ELECTRONICS & COMM. ENGG.
SCMS SCHOOL OF ENGINEERING
AND TECHNOLOGY
KARUKUTTY, ERNAKULAM- 683 576



SCMS SCHOOL OF ENGINEERING AND TECHNOLOGY (AUTONOMOUS)

Vidya Nagar, Palissery, Karukutty, Ernakulam - 683576, Kerala, India.
www.scmsgroup.org/sset

Attendance Sheet

Attendees			
Sl. No	Category	Name	Signature
1	HoD (Chairperson)	Dr.Sreeja K A	 15/7/2026
2	Subject Experts	Dr. Harigovindan V.P. Dean Research and Consultancy, Professor, NIT Puduchery	Attended Online
		Dr. Ajayan K. R. Professor and Dean(Student affairs) (Rtd) CET Thiruvananthapuram	Attended Online
3	Expert Nominated by Vice Chancellor	Dr. B. Bhuvan Assistant Professor (Grade I), Department of Electronics and Communication Engineering, NIT Calicut	Attended Online
4	Representative from Industry	Mr. Prathaban Selvaraj Engineering Leader , Serverless, Google	Attended Online
5	Alumni Representative	Mr. Unnikrishnan Ambady Assistant Manager, Honeywell, Bangalore	Attended Online
Faculty Members from Department			
Sl. No	Name	Designation	Signature
1	Dr. Anandhi.V.	Associate Professor	 15/7/2026
2	Dr. Parvathy.M.	Associate Professor	 15/7/26

3	Ms.Vrinda V.Gopal T	Assistant Professor	<i>Kinda</i> 15/7/26
4	Ms. Praveena S. Kammath	Assistant Professor	<i>Pra Praveena S</i> 15/7/26
5	Ms. Nithya M	Assistant Professor	<i>Nithya</i> 15/7/26
6	Ms. Retty George	Assistant Professor	<i>Retty</i> 15/7/26
7	Ms. Lekshmi Nair M.	Assistant Professor	<i>Lekshmi</i> 15/7/26
8	Ms. Mary Catherine V. G.	Assistant Professor	<i>Mary</i> 15/7/26
9	Ms. Sukanya Singh M. J.	Assistant Professor	<i>Sukanya</i> 15/7/26
10	Mr. Ajit Joseph	Assistant Professor	<i>Ajit</i> 15/7/26
11	Mr. Dinesh Babu M.	Associate Professor	<i>Dinesh</i> 15/7/26
12	Ms. Neethu Susan Idiculay	Assistant Professor	<i>Neethu Susan</i> 15/7/26
13	Ms. Sandhya Venugopal	Assistant Professor	<i>Sandhya</i> 15/7/26
14	Ms. Chelsy Joy	Assistant Professor	<i>Chelsy</i> 15/7/26
15	Ms. Amrutha Joshy	Assistant Professor	<i>Amrutha</i> 15/7/26
16	Ms Athira Anilan	Assistant Professor	<i>Athira</i> 15/7/26
17	Ms Shaila C K	Assistant Professor	<i>Shaila</i> 15/7/26
18	Ms Vishnupriya V	Assistant Professor	<i>Vishnupriya</i> 15/7/26
19	Ms Hridya R P	Assistant Professor	<i>Hridya</i> 15/7/26
20	Mr Unnikrishnan P	Assistant Professor	<i>Unnikrishnan</i> 15/7/26
21	Ms. Lakshmi Raj V	Assistant Professor	<i>Lakshmi</i> 15/7/2026.